

## Product Summary

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(ON)</sub> Max</b>	<b>I<sub>D</sub> T<sub>C</sub> = +25°C</b>
60V	7.2mΩ @ V <sub>GS</sub> = 10V	98A

## Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

## Applications

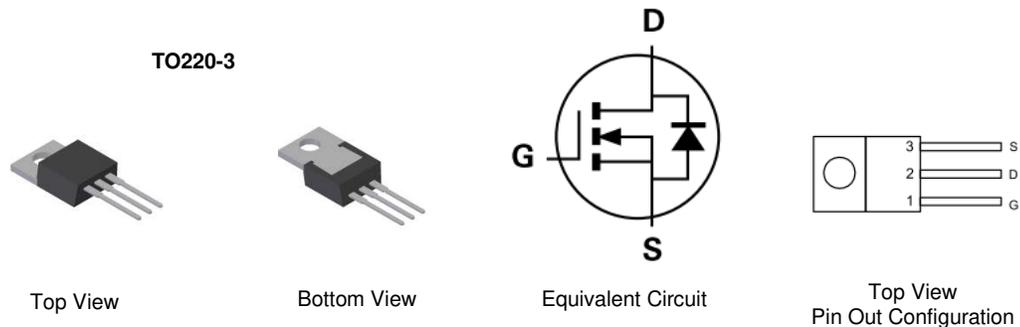
- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

## Features

- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Low Input Capacitance
- Low Input/Output Leakage
- **Lead-Free Finish; RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**

## Mechanical Data

- Case: TO220-3
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 1.85 grams (Approximate)

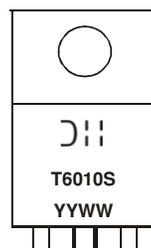


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6010SCT	TO220-3	50 Pieces/Tube

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



DII = Manufacturer's Marking  
 T6010S = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Last Digit of Year (ex: 16 = 2016)  
 WW or WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6)	I <sub>D</sub>	T <sub>C</sub> = +25°C	98
		T <sub>C</sub> = +70°C	78
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	160	A
Avalanche Current, L=0.1mH	I <sub>AS</sub>	20	A
Avalanche Energy, L=0.1mH	E <sub>AS</sub>	20	mJ

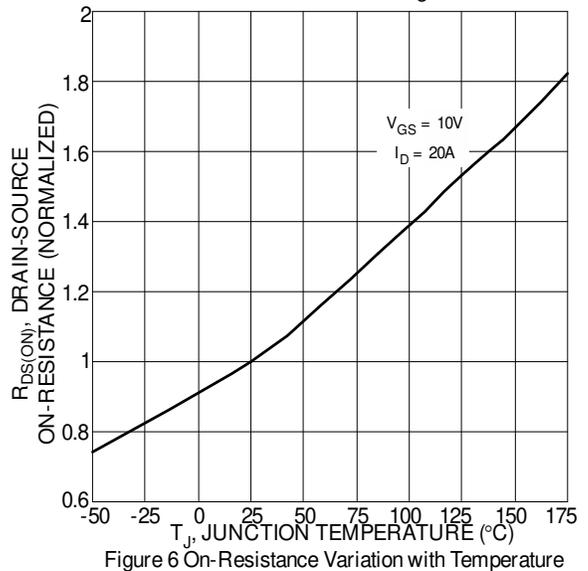
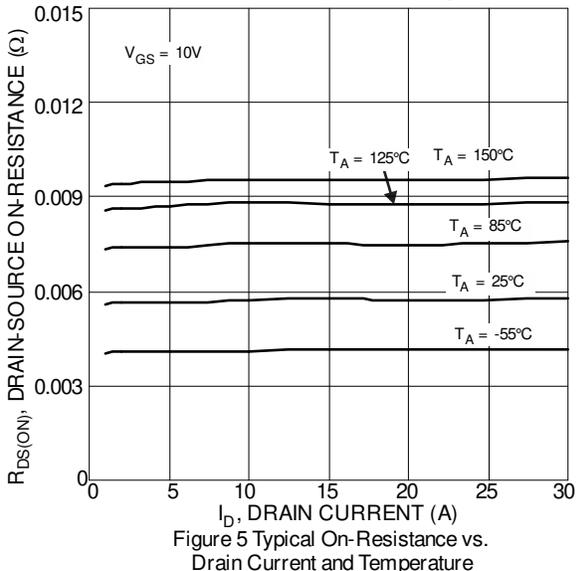
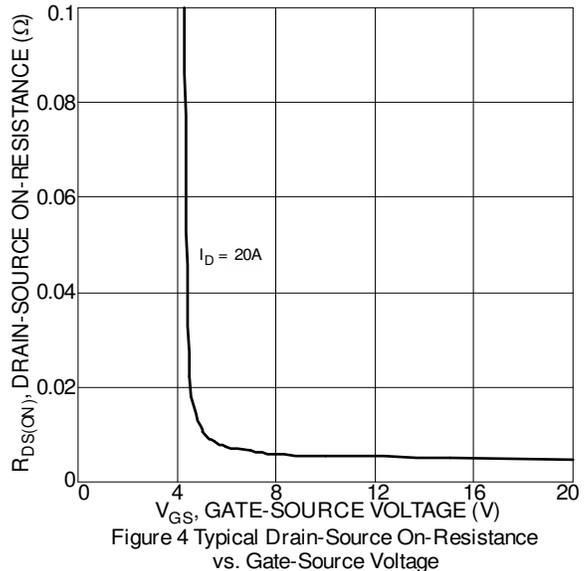
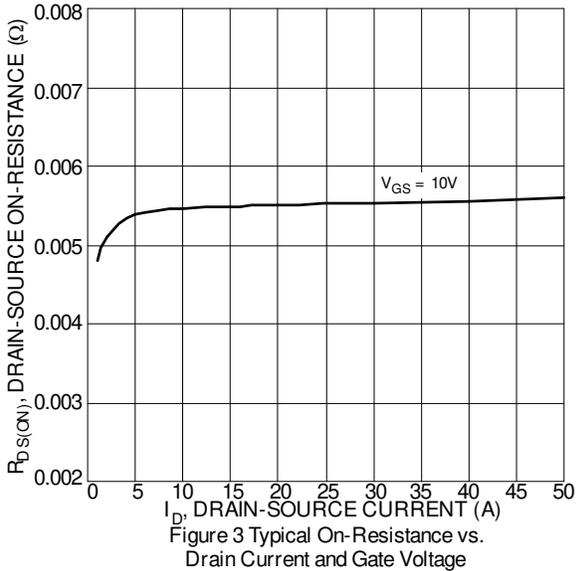
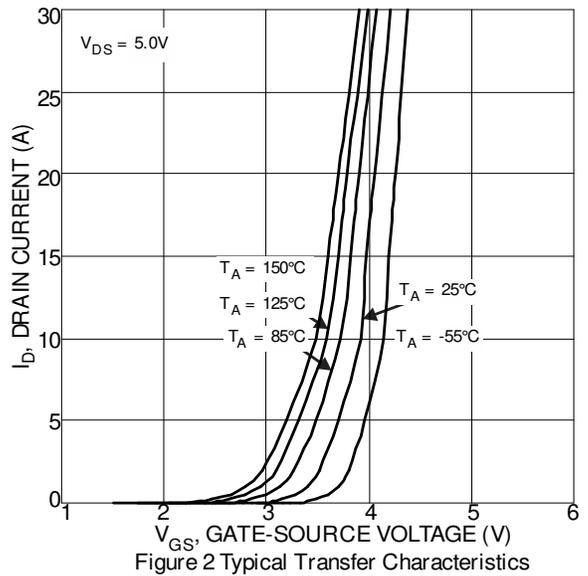
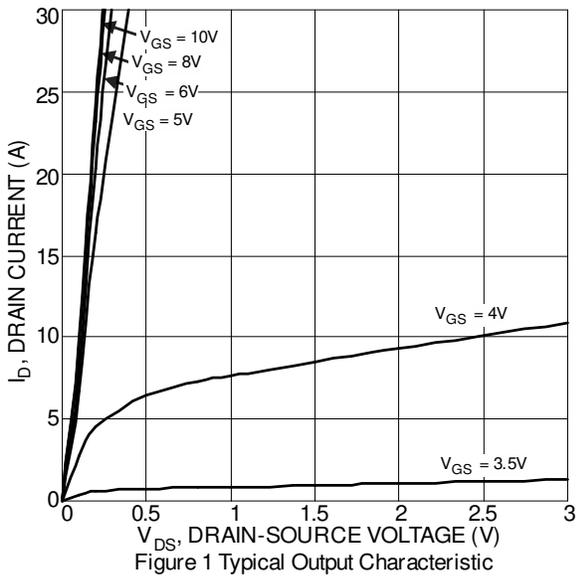
**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	2.3	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	52.8	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	104	W
Thermal Resistance, Junction to Case (Note 6)	R <sub>θJC</sub>	1.2	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	—	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	5.5	7.2	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A
Diode Forward Voltage	V <sub>SD</sub>	—	—	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	C <sub>ISS</sub>	—	1,940	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>OSS</sub>	—	759	—		
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	85.2	—		
Gate Resistance	R <sub>G</sub>	—	0.55	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge	Q <sub>G</sub>	—	36.3	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A, V <sub>GS</sub> = 10V
Gate-Source Charge	Q <sub>GS</sub>	—	7.5	—		
Gate-Drain Charge	Q <sub>GD</sub>	—	10.5	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.7	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A, R <sub>G</sub> = 3Ω
Turn-On Rise Time	t <sub>R</sub>	—	10.4	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	16.3	—		
Turn-Off Fall Time	t <sub>F</sub>	—	11.2	—		
Reverse Recovery Time	t <sub>RR</sub>	—	35.6	—	ns	I <sub>F</sub> = 20A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	37.9	—	nC	

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  6. Device mounted on infinite heat sink.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.



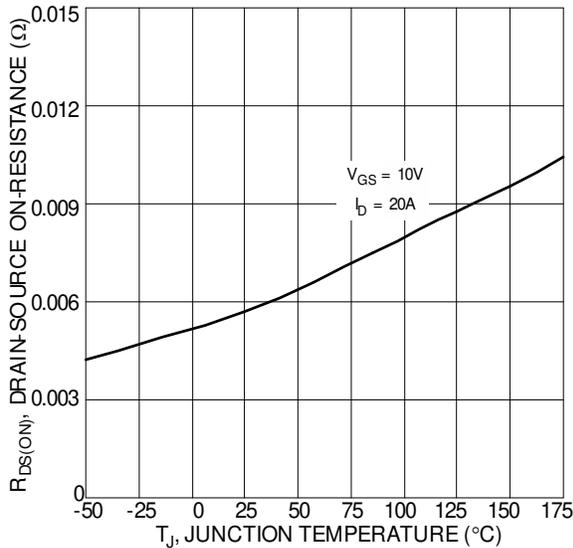


Figure 7 On-Resistance Variation with Temperature

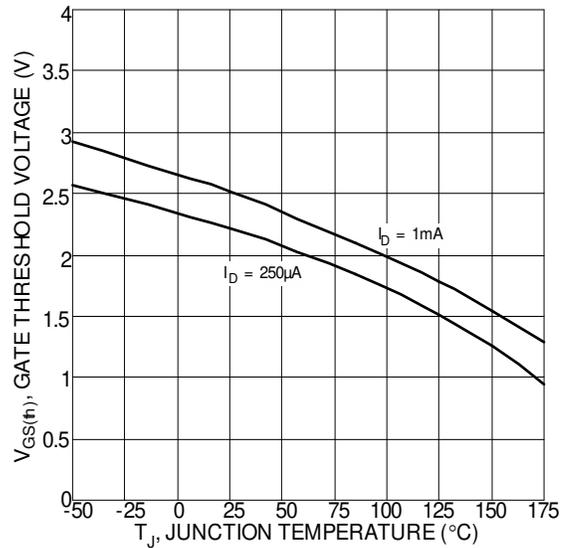


Figure 8 Gate Threshold Variation vs. Ambient Temperature

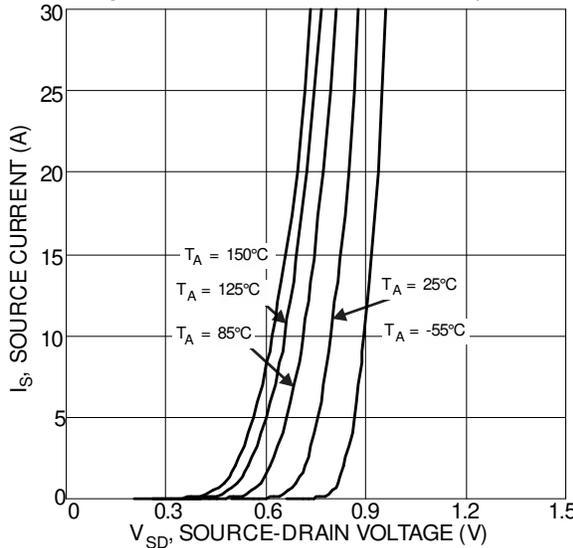


Figure 9 Diode Forward Voltage vs. Current

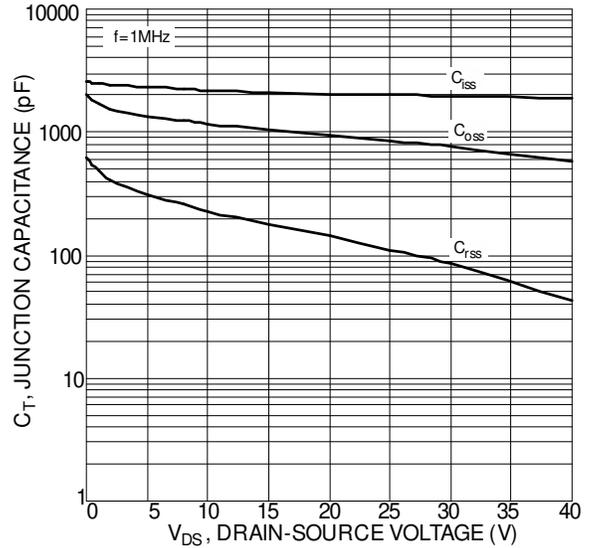


Figure 10 Typical Junction Capacitance

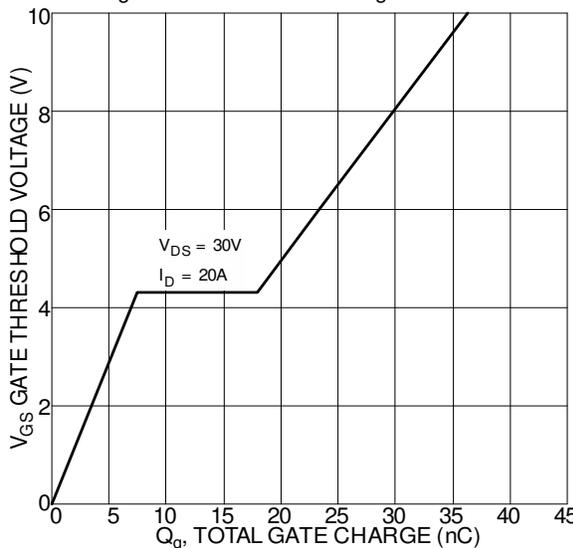


Figure 11 Gate Charge

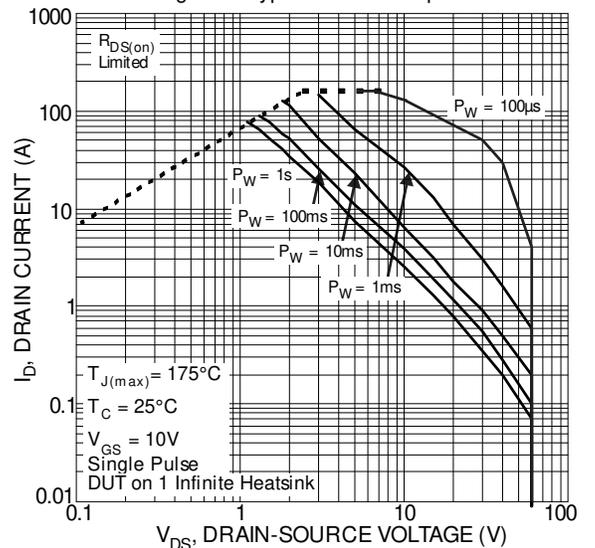
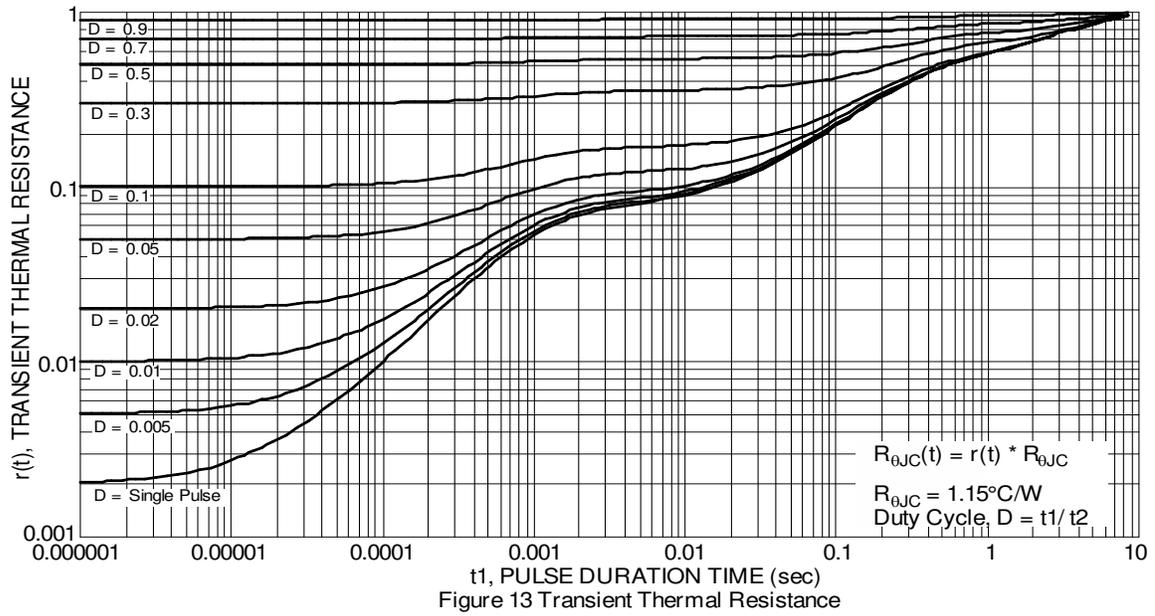


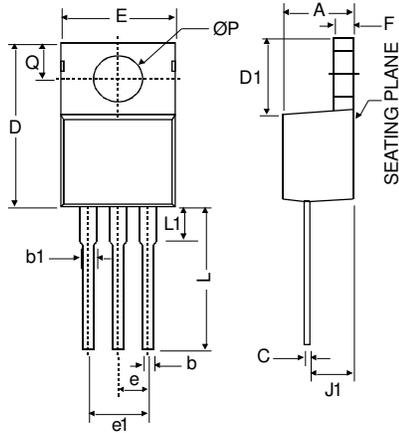
Figure 12 SOA, Safe Operation Area



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**TO220-3**



TO220-3		
Dim	Min	Max
<b>A</b>	3.55	4.85
<b>b</b>	0.51	1.14
<b>b1</b>	1.14	1.78
<b>C</b>	0.31	1.14
<b>D</b>	14.20	16.50
<b>D1</b>	5.84	6.86
<b>E</b>	9.70	10.70
<b>e</b>	2.79	2.99
<b>e1</b>	4.83	5.33
<b>F</b>	0.51	1.40
<b>J1</b>	2.03	2.92
<b>L</b>	12.72	14.72
<b>L1</b>	3.66	6.35
<b>P</b>	3.53	4.09
<b>Q</b>	2.54	3.43
<b>All Dimensions in mm</b>		

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