

DVB-T2/T/C Demodulator

OVERVIEW

- Conforms to the following standards:
 - ETSI EN 302-755 V1.3.1 (DVB-T2/T2-Lite)
 - ETSI EN 300-744 V1.6.1 (DVB-T)
 - ETSI EN 300-429 V1.2.1 (DVB-C)
- DVB-T2/T Compliant with:
 - DTG D-Book 7 part A V3.0
 - Nordig Unified Test specification V2.2.2
- Flexible architecture allows for easy upgrade via firmware
- Fast switching between standards
- Automatic acquisition modes

FEATURES

DVB-T2

- Compliant with DTG D-book 7 part A version 3.0 and Nordig 2.2.2.
- Supports all DVB-T2 modes including
 - Single and multiple-PLPs
 - SISO and MISO transmissions
 - Future Extension Frames (FEFs)
 - DVB-T2 Base and Lite profiles
 - L1 post descrambling and long FEF
- Automatic T/T2 standard detection
- Null packet insertion
- QAM modes: 4, 16, 64, 256 (with or without rotation)
- Bandwidths: 1.7, 5.0, 6.0, 7.0, 8.0 MHz
- FFT Sizes: 1K, 2K, 4K, 8K, 16K, 32K
- Automatic merging of common and data PLPs
- Improved performance and acquisition time versus previous generation

DVB-T

- Compliant with DTG D-book 7 part A version 3.0 and Nordig 2.2.2.
- Improved AWGN performance versus previous generation

DVB-C

- Fully compliant with the Broadcast Service profile of the European Standard ETSI EN 300 429 v1.2.1 (DVB-C).
- Supports all five QAM modes: 16QAM, 32QAM, 64QAM, 128QAM and 256 QAM
- Automatic modulation detection
- ± 500 KHz frequency acquisition range.
- ± 1000 ppm symbol timing acquisition range.
- 0.87 to 9 Msps symbol rate

FEATURES

General

- Low IF and high IF(36 MHz) mode input
 - Performance upgrade via firmware
 - Fast 400KHz I2C compatible bus interface
 - MPEG output and AGC can be set to Tri-state
 - 48 pin QFN 6 x 6 mm package
 - Pin-to-Pin compliant with Availink chips
- AVL6862: DVB-C/T/T2 + DVB-S/S2

AVL6812: ISDB-T + DVB -S/S2 + J83.B

AVL6882: DVB-C/T/T2 + ISDB-T +
DVB-S/S2 + J83.B

AVL6381: DTMB + DVB-C

AVL6762 Block Diagram

